IN THE CLAIMS:

Please amend the claims as follows:

1. (Cancelled) ~~A semiconductor integrated circuit device comprising:~~

~~an integrated circuit; and~~

~~a shield wiring layer formed above the integrated circuit and preventing a physical alteration to the integrated circuit,~~

~~wherein the shield wiring layer includes a lower shield line and an upper shield line formed above the lower shield line, and~~

~~the directions in which the lower shield line and the upper shield line are arranged intersect each other.~~

2. (Currently Amended) ~~The device of claim 1,~~

~~wherein the lower shield line comprises a plurality of lower shield lines and the upper shield line comprises a plurality of upper shield lines, and~~

A semiconductor integrated circuit device comprising:

an integrated circuit; and

a shield wiring layer formed above the integrated circuit and preventing a physical alteration to the integrated circuit,

wherein the shield wiring layer includes a lower shield line and an upper shield line formed above the lower shield line,

the directions in which the lower shield line and the upper shield line are arranged intersect each other,

the lower shield line comprises a plurality of lower shield lines and the upper shield line comprises a plurality of upper shield lines, and

the device further comprises a selector circuit which electrically connects at least two of the plurality of lower shield lines and/or the plurality of upper shield lines and which can change the line targeted for this connection.

3. (Original) The device of claim 2,

wherein the selector circuit comprises a plurality of selector circuits, and

the plurality of selector circuits are provided above the integrated circuit to have random intervals.

4. (Currently Amended) ~~The device of claim 1, wherein~~

A semiconductor integrated circuit device comprising:

an integrated circuit; and

a shield wiring layer formed above the integrated circuit and preventing a physical alteration to the integrated circuit,

wherein the shield wiring layer includes a lower shield line and an upper shield line formed above the lower shield line,

the directions in which the lower shield line and the upper shield line are arranged intersect each other, and

at least one of the lower shield line and the upper shield line has a connector connected to a power source line, a ground line, or a signal line for controlling the integrated circuit.

5. (Original) The device of claim 4,

wherein the connector comprises a plurality of connectors and the plurality of connectors are provided above at least one of the lower shield line and the upper shield line, and

the plurality of connectors are provided above the integrated circuit to have random intervals.

6. (Original) The device of claim 5,

wherein at least one of the lower shield line and the upper shield line is formed with a plurality of dummy vias randomly arranged without being electrically connected to the signal line.

7. (Original) A semiconductor integrated circuit device comprising:

an integrated circuit; and

a shield line formed above the integrated circuit and preventing a physical alteration to the integrated circuit,

wherein the directions in which the shield line and an interconnection line of the integrated circuit are arranged diagonally intersect each other.

8. (Original) The device of claim 7,

wherein the shield line comprises a plurality of shield lines, and

the device further comprises a selector circuit which electrically connects at least two of the plurality of shield lines and which can change the line targeted for this connection.

9. (Original) The device of claim 8,

wherein the selector circuit comprises a plurality of selector circuits, and

the plurality of selector circuits are provided above the integrated circuit to have random intervals.

10. (Original) The device of claim 7, wherein the shield line has a connector connected to a power source line, a ground line, or a signal line for controlling the integrated circuit.

11. (Original) The device of claim 10,

wherein the connector comprises a plurality of connectors and the plurality of connectors are provided above the shield line, and

the plurality of connectors are provided above the integrated circuit to have random intervals.

12. (Original) The device of claim 11, wherein the shield line is formed with a plurality of dummy vias randomly arranged without being electrically connected to the signal line.